

**ECS Configuration Change Request**

1. Originator Sarah Lewallen	2. Log Date: 11/30/00	3. CCR #: 00-1116	4. Rev:	5. Tel: 301-925-0865	6. Rm #: 2117b	7. Dept. SED
8. CCR Title: Hardware installations for SPR and FSMS at LaRC; CPU upgrade at GSFC; Gigabit card move to EDC.						
9. Originator Signature/Date <i>Sarah Lewallen</i>			10. Class II	11. Type: CCR	12. Need Date: 12/15/00	
13. Office Manager Signature/Date <i>Randy Hager</i>			14. Category of Change: Update ECS Baseline Doc.		15. Priority: (If "Emergency" fill in Block 28). Routine	
16. Documentation/Drawings Impacted: see attached			17. Schedule Impact:	18. CI(s) Affected: i0spg10, i0drg04, g0spg10, e0acq11		
19. Release Affected by this Change: N/A		20. Date due to Customer: 12/30/00		21. Estimated Cost: None - Under 100K		
22. Source Reference: <input type="checkbox"/> NCR (attach) <input type="checkbox"/> Action Item <input type="checkbox"/> Tech Ref. <input type="checkbox"/> GSFC <input checked="" type="checkbox"/> Other: Procurement - CCR 00-0628						
23. Problem: (use additional Sheets if necessary) Need additional processing power in SPR at LaRC and GSFC. Need increased capacity in DRG to support conversion of D3 to 9940 and still maintain the current archive performance.						
24. Proposed Solution: (use additional sheets if necessary) An Origin 3000 will be arriving at LaRC. Install the new 32 CPU 3000 and remove the 16 CPU 2000. The 3000 will be connected to the existing SPR RAID. The Origin 2000 will be moved to the FSMS subsystem as host "i0drg04" This will be used to host Silo 1 during conversion, and will replace i0drg01 as the primary FSMS server after conversion. It will be added to the network and configured with 8CPUs and 4GB of RAM using IRIX 6.5.6 and 100 base T. 8 CPUs and the the balance of memory will be moved to GSFC for g0spg10. The Gigabit ethernet card will be moved to EDC for e0acq11. Install serial Hippi card for i0drg04.						
25. Alternate Solution: (use additional sheets if necessary) Install the Origin 3000 and ship the entire 2000 to GSFC for SPR.						
26. Consequences if Change(s) are not approved: (use additional sheets if necessary) Processing requirements from the F&PRS at GSFC and LaRC will not be met.						
27. Justification for Emergency (If Block 15 is "Emergency"):						
28. Site(s) Affected: <input type="checkbox"/> EDF <input type="checkbox"/> PVC <input type="checkbox"/> VATC <input checked="" type="checkbox"/> EDC <input checked="" type="checkbox"/> GSFC <input checked="" type="checkbox"/> LaRC <input type="checkbox"/> NSIDC <input type="checkbox"/> SMC <input type="checkbox"/> AK <input type="checkbox"/> JPL <input type="checkbox"/> EOC <input type="checkbox"/> IDG Test Cell <input type="checkbox"/> Other						
29. Board Comments:				30. Work Assigned To:	31. CCR Closed Date:	
32. EDF/SCDV CCB Chair (Sign/Date):			Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS			
33. M&O CCB Chair (Sign/Date):			Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ECS			
34. ECS CCB Chair (Sign/Date):			Disposition: Approved App/Com. Disapproved Withdraw Fwd/ESDIS ERB Fwd/ESDIS			

**ORIGINAL**

## ADDITIONAL SHEET

CCR #: 00-1114 Rev: Originator: Sarah Lewallen

Telephone: 301-925-0865 Office: 2117b

**Title of Change:** Hardware installations for SPR and FSMS at LaRC; CPU upgrade at GSFC; Gigabit card move to EDC.

Documents impacted

923-TDL-007

921-TDL-003

923-TDL-002

923-TDL-005

921-TDL-002

920-TDE-001

920-TDG-001

920-TDL-001

922-TDL-007

920-TDL-002 - this document will not be updated until IRIX 6.5.6 moves into production

All hardware and infrastructure maps already show these systems and power is in place

CM01AJA00

ORIGINAL